



**SMA New Energy Technology (Jiangsu) Co., Ltd**  
No. 198 Xiangyang Road  
215011 Suzhou • China  
Tel: +86 512 6937 0998  
Fax: +86 512 6937 3159

## Manufacture's Declaration

### Confirmation of Compliance with the Requirements of VFR 2014/UTE C 15-712-1

**SMA New Energy Technology (Jiangsu) Co., Ltd** hereby confirms that the inverter types listed below table meet the requirement of the French code of practice UTE C 15-712-1.

<b>Brand</b>	zeversolar	
<b>Type reference</b>	Evershine TLC10000	Evershine TLC8000
<b>Nominal AC Power</b>	10000W	8000W
<b>Maximum AC Power</b>	10000W	8800W

The inverters meet the requirements of VFR 2014/UTE C 15-712-1, along with the specifications in the data sheet and the CE declaration, by the following points:

- The certificate of the compliance with VDE 0126-1-1/A1:2012 has been issued by an accredited institute. The certificate can be downloaded from the website <http://www.zeversolar.com/>.
- The inverters meet the requirements of the French code of practice UTE C 15-712-1.
- The automatic disconnection devices integrated within the inverters with three-phase mains surveillance meet the requirements of DIN VDE 0126-1-1 / A1: 2012 with the deviation of VFR 2014 (mentioned in "Protections des installations de production raccordées au réseau public de distribution, ERDF-NOI-RES\_13E, Version 5, 30/06/2013").
- The grid protection parameters can't be changed by user, an installer or by any person other than SMA (password protected).

Suzhou, 31.07.2017

**SMA New Energy Technology (Jiangsu) Co., Ltd.**

Sandy Gong, Safety Manager



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## Test Results

### Power quality

Harmonic current emissions as per EN 61000-3-2							
Harmonic	Test Value in Amps Phase 1	% of fund Phase 1	Test Value in Amps Phase 2	% of fund Phase 2	Test Value in Amps Phase 3	% of fund Phase 3	Limit value in Amps
2	0.027	0.188	0.028	0.190	0.026	0.182	1.080
3	0.016	0.110	0.012	0.083	0.019	0.133	2.300
4	0.025	0.170	0.017	0.113	0.024	0.167	0.430
5	0.116	0.799	0.125	0.862	0.117	0.811	1.140
6	0.010	0.065	0.006	0.043	0.009	0.066	0.300
7	0.074	0.509	0.075	0.517	0.063	0.439	0.770
8	0.030	0.203	0.027	0.184	0.034	0.236	0.230
9	0.011	0.075	0.004	0.026	0.006	0.039	0.400
10	0.038	0.258	0.034	0.232	0.036	0.252	0.184
11	0.059	0.406	0.055	0.380	0.061	0.420	0.330
12	0.004	0.029	0.003	0.022	0.006	0.039	0.153
13	0.054	0.373	0.050	0.345	0.050	0.347	0.210
14	0.009	0.063	0.008	0.057	0.007	0.045	0.131
15	0.003	0.019	0.004	0.026	0.004	0.029	0.150
16	0.005	0.034	0.004	0.025	0.006	0.038	0.115
17	0.036	0.246	0.037	0.257	0.037	0.258	0.132
18	0.004	0.027	0.003	0.019	0.004	0.028	0.102
19	0.038	0.264	0.040	0.273	0.036	0.248	0.118
20	0.003	0.022	0.004	0.024	0.003	0.020	0.092
21	0.007	0.048	0.004	0.024	0.003	0.022	0.107
22	0.004	0.030	0.005	0.034	0.003	0.021	0.084
23	0.034	0.231	0.031	0.212	0.036	0.249	0.098
24	0.004	0.025	0.003	0.019	0.004	0.030	0.077
25	0.033	0.227	0.030	0.208	0.030	0.208	0.090
26	0.005	0.037	0.005	0.037	0.007	0.050	0.071
27	0.003	0.018	0.003	0.022	0.003	0.021	0.083
28	0.005	0.037	0.004	0.027	0.004	0.028	0.066
29	0.020	0.136	0.021	0.144	0.021	0.148	0.078
30	0.003	0.019	0.002	0.015	0.003	0.022	0.061
31	0.022	0.149	0.023	0.159	0.021	0.145	0.073
32	0.004	0.028	0.004	0.028	0.003	0.023	0.058

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33	0.005	0.035	0.003	0.018	0.002	0.016	0.068
34	0.004	0.024	0.004	0.030	0.004	0.031	0.054
35	0.016	0.112	0.015	0.102	0.018	0.127	0.064
36	0.003	0.019	0.002	0.012	0.002	0.017	0.051
37	0.014	0.093	0.012	0.084	0.012	0.082	0.061
38	0.002	0.016	0.003	0.022	0.004	0.027	0.048
39	0.002	0.013	0.002	0.017	0.002	0.012	0.058
40	0.003	0.020	0.003	0.019	0.002	0.015	0.046
THD	-	1.320%	-	1.324%	-	1.302%	-

Voltage fluctuations and flicker as per EN 61000-3-3								
	Starting			Stopping			Running	
	dmax	dc	d(t)	dmax	dc	d(t)	Pst	Plt 2 hours
Measured Values	1.03%	1.13%	1.10%	0.81%	0.84%	0.82%	0.188	0.157
Limits set under EN 61000-3-3	4%	3.3%	3.3% 500ms	4%	3.3%	3.3% 500ms	1.0	0.65
Test start date	08/10/2014			Test end date			08/10/2014	
Test location	Audix Technology (Wujiang) Co., Ltd. EMC Dept							

Power factor *			
Test Voltage level	210 V	230 V	253 V
Measured value at 100%Pn	0.998	0.999	0.999
Limit	>0.95	>0.95	>0.95

\* Measured at three voltage levels and at full output. The voltage maintained within  $\pm 1.5\%$  of the stated level during the test.

### Under/Over frequency protection

Function	Limit		Actual setting		Trip test	
	Frequency[Hz]	Time[s]	Frequency[Hz]	Time[s]	Frequency[Hz]	Time[s]
U/F Stage 1	47.5	0.2	47.5	0.16	47.51	0.089
O/F Stage 1	50.6	0.2	50.6	0.16	50.60	0.110

### Under/Over voltage protection

Function	Limit		Actual setting		Trip test	
	Voltage [V]	Time [s]	Voltage [V]	Time [s]	Voltage [V]	Time [s]
U/V Stage 1_phase 1	184.0	0.2	184.0	0.12	184.3	0.127
U/V Stage 1_phase 2	184.0	0.2	184.0	0.12	184.5	0.117

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U/V Stage 1_phase 3	184.0	0.2	184.0	0.12	184.8	0.127
O/V Stage 1*_phase 1	253.0	600	253.0	600	257.6	516
O/V Stage 1*_phase 2	253.0	600	253.0	600	257.6	516
O/V Stage 1*_phase 3	253.0	600	253.0	600	257.6	516
O/V Stage 2_phase 1	264.5	0.2	264.5	0.12	264.3	0.134
O/V Stage 2_phase 2	264.5	0.2	264.5	0.12	265.1	0.132
O/V Stage 2_phase 3	264.5	0.2	264.5	0.12	264.8	0.128

\*Over voltage - stage 1: 10 min mean value corresponding to EN 50160. The voltage is set to 100%Un and held for 600s. After that, the voltage is set to 112%Un. It must be switched off within 600s.

### Loss of mains test

Method used	Reactive power disturbed		
	25% Pn	50% Pn	100% Pn
Limit according to VDE 0126-1-1	5s	5s	5s
Trip time (L:+5%)	0.081s	0.119s	0.106s
Trip time (L:+4%)	0.115s	0.118s	0.108s
Trip time (L:+3%)	0.114s	0.155s	0.128s
Trip time (L:+2%)	0.090s	0.158s	0.129s
Trip time (L:+1%)	0.081s	0.152s	0.112s
Trip time (L:+0%)	0.096s	0.151s	0.103s
Trip time (L:-1%)	0.085s	0.118s	0.119s
Trip time (L:-2%)	0.095s	0.132s	0.118s
Trip time (L:-3%)	0.082s	0.133s	0.124s
Trip time (L:-4%)	0.084s	0.159s	0.122s
Trip time (L:-5%)	0.066s	0.115s	0.104s

### Reconnection time measurement

Reconnection time	Under/over Voltage	Under / over Frequency	Loss of Mains
Limit	60s	60s	60s
Actual setting	60s	60s	60s
Recorded value	68s	67s	67s

### DC current monitoring

A direct current feed to the low voltage grid due to a defective generator operation must lead to a disconnection within 0.2 s. (according to VDE 0126-1-1)

Function	Limit		Trip test	
	DC current [A]	Time [ms]	DC current [A]	Time [ms]

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Positive DC current	1.0	200	0.98	170.5
Negative DC current	1.0	200	0.99	163.5

## Residual current monitoring

Test for correct triggering in event of steadily rising residual current				
	Limit		Trip test	
PV connection	Fault current [mA]	Time [ms]	Test Current [mA]	Time [ms]
PV+	300	300	94	102.5
PV-	300	300	91	110.0

Test for correct triggering in event of sudden rising residual current				
	Limit		Trip test	
PV connection	Fault current [mA]	Time [ms]	Test Current [mA]	Time [ms]
PV+	30	300	28.7	185.5
PV+	60	150	59.5	86.5
PV+	150	40	148.2	29.6
PV-	30	300	30.5	182.0
PV-	60	150	60.5	99.5
PV-	150	40	150.2	29.8

## Array insulation resistance detection

The value of the total resistance, including the intentional resistance for array functional grounding, the expected insulation resistance of the array to ground, and the resistance of any other networks connected to ground (for example measurement networks) must not be lower than  $R = (V_{MAX PV} / 30 \text{ mA})$  ohms. (according to EN 62109-2)

PV connector	Test resistance value	Activation(Yes/No)	Display
PV+	40kΩ	Yes	Isolation Fault
PV-	40kΩ	Yes	Isolation Fault

## Additional comments

Evershine TLC8000 is similar to Evershine TLC10000 in circuit and construction except for the output rating of current and power. The test result can refer to Evershine TLC10000.

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